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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a CON of 09/995,594 11/29/2001 PAT 6,835,981

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Yes

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2001-298533 09/27/2001

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Yes

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 06/29/2004

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 19	TOTAL CLAIMS 20	INDEPENDENT CLAIMS 2
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after?				
Verified and Acknowledged	Examiner's signature	Initials		

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## TITLE

Semiconductor chip which combines bulk and SOI regions and separates same with plural isolation regions

☐ All Fees☐ 1.16 Fees ( Filing )

<b>FILING FEE</b>  <b>RECEIVED</b> 770	<b>FEES:</b> Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue ) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
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